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AUG 03 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**Group Art Unit: 2822**  
**Examiner: Kiesha L. Rose**  
**Confirmation No.:**

**In re PATENT APPLICATION of:**  
**Applicant(s): Motoki KOBAYASHI et al.**

**Serial No.:** 10/673,499

**Filing Date:** September 30, 2003

**For: SEMICONDUCTOR DEVICE AND  
SEMICONDUCTOR DEVICE  
FABRICATION METHOD**

Atty. Dkt.: FUJI 137

## AMENDMENT

**Commissioner for Patents**  
**P.O. Box 1450**  
**Alexandria, VA 22313-1450**

Sir:

This paper is in response to the Official Action mailed on May 4, 2005. No fee is due. However, please charge our Deposit Account No. 18-0002 if any fees are needed to enter this paper, and please advise us accordingly. It is noted that no petition is required because of the authorization to charge, but please consider this paper a petition for extension of time if needed.

The amendments to the claims are supported in the fourth through sixth paragraphs under the heading **First Embodiment in the Detailed Description**, and in Fig. 2.